

CLAIMS:

1. A feedback oscillator device formed with an integrated circuit, said device comprising:
 - a semiconductor material substrate having a conductive ground plane on an equipotential major surface thereof on a side opposite an operating major surface thereof;
 - an amplifier having an input and an output provided at least in part on said semiconductor material substrate at said operating surface thereof, said amplifier being capable of providing signals at said output thereof representative of signals occurring at said input thereof; and
 - a coupler having an oblong input conductor of a selected input length provided on said operating surface of said semiconductor substrate electrically coupled at one end thereof to said amplifier output, and further having an oblong output conductor of a selected output length provided on said operating surface of said semiconductor substrate closely adjacent to said input conductor with said input length being substantially parallel to said output length, said output conductor being electrically coupled at one end thereof to said amplifier input.
2. The device of claim 1 wherein said semiconductor material substrate is formed of gallium arsenide.
3. The device of claim 1 further comprising a capacitor electrically coupling said coupler output conductor to said amplifier input

4. The device of claim 1 further comprising a transfer system electrically coupling said coupler output conductor to said amplifier input, said transfer system having an oblong input conductor of a selected input length provided on said operating surface of said semiconductor substrate electrically coupled substantially on said operating surface at one end thereof to said coupler output conductor but free of other connections on said opposite end thereof to other structures on said operating surface, and having an oblong output conductor of a selected output length provided on said operating surface of said semiconductor substrate electrically coupled substantially on said operating surface at one end thereof to said amplifier input but free of other connections on said opposite end thereof to other structures on said operating surface.

5. The device of claim 1 wherein said amplifier comprises a transistor having first and second terminating regions and having a control region therein by which it is capable of being directed, by electrical energization thereof, to effectively provide a conductive path of a selected conductivity between said first and second terminating regions, a selected one of said first and second terminating regions being coupled to said amplifier output and said control region being coupled to said amplifier input.

6. The device of claim 1 wherein said coupler input conductor has a continuous portion thereof on said operating surface electrically coupled at one end thereof to said amplifier output and has at an opposite end thereof, and connected thereto, an input extension comprising a sequence of conductive pads on said operating surface interconnected by an elevated conductor having portions between said conductive pads that are relatively easily removable, and wherein said coupler output conductor has a continuous portion thereof on said operating surface electrically coupled at one end thereof to said amplifier input and has at an opposite

end thereof, and connected thereto, an output extension comprising a sequence of conductive pads on said operating surface interconnected by an elevated conductor having portions between said conductive pads that are relatively easily removable.

7. The device of claim 3 wherein said capacitor is formed by a varactor having a conductor connected thereto through which a low frequency voltage can be further provided across a semiconductor material junction in said varactor.

8. The device of claim 4 further comprising a capacitor electrically coupling said transfer system output conductor to said amplifier input.

9. The device of claim 4 wherein said transfer system input conductor has a continuous portion thereof on said operating surface electrically coupled at one end thereof to said coupler output conductor and has at an opposite end thereof, and connected thereto, an input extension comprising a sequence of conductive pads on said operating surface interconnected by an elevated conductor having portions between said conductive pads that are relatively easily removable, and wherein said transfer system output conductor has a continuous portion thereof on said operating surface electrically coupled at one end thereof to said amplifier input and has at an opposite end thereof, and connected thereto, an output extension comprising a sequence of conductive pads on said operating surface interconnected by an elevated conductor having portions between said conductive pads that are relatively easily removable.

10. The device of claim 5 wherein said semiconductor material substrate is formed of gallium arsenide.

11. The device of claim 5 wherein said transistor is a high electron mobility transistor.
12. The device of claim 5 wherein said transistor where that remaining one of said first and second terminating regions has an inductance coupled therefrom to said conductive ground plane.
13. The device of claim 6 further comprising a transfer system electrically coupling said coupler output conductor to said amplifier input, said transfer system having an oblong input conductor of a selected input length provided on said operating surface of said semiconductor substrate electrically coupled substantially on said operating surface at one end thereof to said coupler output conductor but free of other connections on said opposite end thereof to other structures on said operating surface, and having an oblong output conductor of a selected output length provided on said operating surface of said semiconductor substrate electrically coupled substantially on said operating surface at one end thereof to said amplifier input but free of other connections on said opposite end thereof to other structures on said operating surface, and wherein said transfer system input conductor has a continuous portion thereof on said operating surface electrically coupled at one end thereof to said coupler output conductor and has at an opposite end thereof, and connected thereto, an input extension comprising a sequence of conductive pads on said operating surface interconnected by an elevated conductor having portions between said conductive pads that are relatively easily removable, and wherein said transfer system output conductor has a continuous portion thereof on said operating surface electrically coupled at one end thereof to said amplifier input and has at an opposite end thereof, and connected thereto, an output extension comprising a sequence of conductive pads on said

operating surface interconnected by an elevated conductor having portions between said conductive pads that are relatively easily removable.

14. The device of claim 8 wherein said capacitor is formed by a varactor having a conductor connected thereto through which a low frequency voltage can be further provided across a semiconductor material junction in said varactor.

15. The device of claim 11 wherein said transistor is a pseudomorphic high electron mobility transistor.

16. The device of claim 13 further comprising a capacitor electrically coupling said transfer system output conductor to said amplifier input.

17. The device of claim 16 wherein said capacitor is formed by a varactor having a conductor connected thereto through which a low frequency voltage can be further provided across a semiconductor material junction in said varactor.

18. A feedback oscillator device formed with an integrated circuit, said device comprising:

- a semiconductor material substrate having a conductive ground plane on an equipotential major surface thereof on a side opposite an operating major surface thereof;

- an amplifier having an input and an output provided at least in part on said semiconductor material substrate at said operating surface thereof, said amplifier being capable of providing signals at said output thereof representative of signals occurring at said input thereof; and

a transfer system having an oblong input conductor of a selected input length provided on said operating surface of said semiconductor substrate electrically coupled substantially on said operating surface at one end thereof to said amplifier output but free of other connections on said opposite end thereof to other structures on said operating surface, and having an oblong output conductor of a selected output length provided on said operating surface of said semiconductor substrate electrically coupled substantially on said operating surface at one end thereof to said amplifier input but free of other connections on said opposite end thereof to other structures on said operating surface.

19. The device of claim 18 wherein said semiconductor material substrate is formed of gallium arsenide.

20. The device of claim 18 further comprising a capacitor electrically coupling said transfer system output conductor to said amplifier input.

21. The device of claim 19 wherein said amplifier comprises a transistor having first and second terminating regions and having a control region therein by which it is capable of being directed, by electrical energization thereof, to effectively provide a conductive path of a selected conductivity between said first and second terminating regions, a selected one of said first and second terminating regions being coupled to said amplifier output and said control region being coupled to said amplifier input.

22. The device of claim 20 wherein said capacitor and said transfer system have a circuit portion in series therewith having an impedance therein dependant on frequency exhibiting an extreme impedance value at a resonant frequency.

23. The device of claim 21 wherein said transistor where that remaining one of said first and second terminating regions has an inductance coupled therefrom to said conductive ground plane.

24. The device of claim 22 wherein said capacitor is formed by a varactor having a conductor connected thereto through which a low frequency voltage can be further provided across a semiconductor material junction in said varactor.